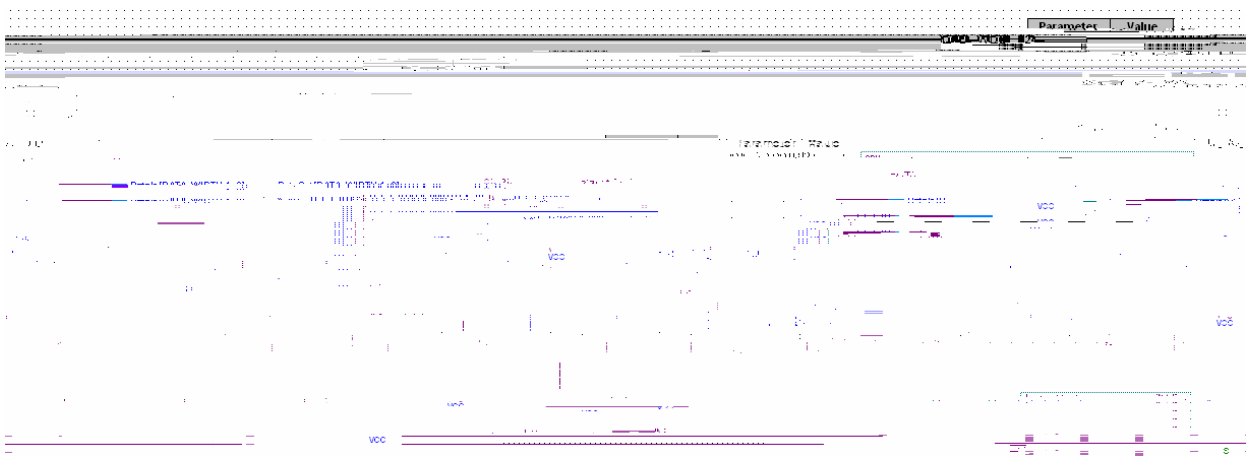
Fig. 2.

The first figure corresponds to PC implemented in general purpose register block, and the second – to PC implemented as a standalone register. The first variant requires less hardware resources but it is almost 2 times slower then the second variant. Register blocks RG A and RG B contain

Type / Data Width	12 bit	16 bit	24 bit	32 bit

Condition code	Mnemonic	Meaning

Bit	5...	4	3	2	1	0
Meaning	Reserved	I	C	O	N	Z


Parameter
Value
VCC
VCC
VCC
VCC