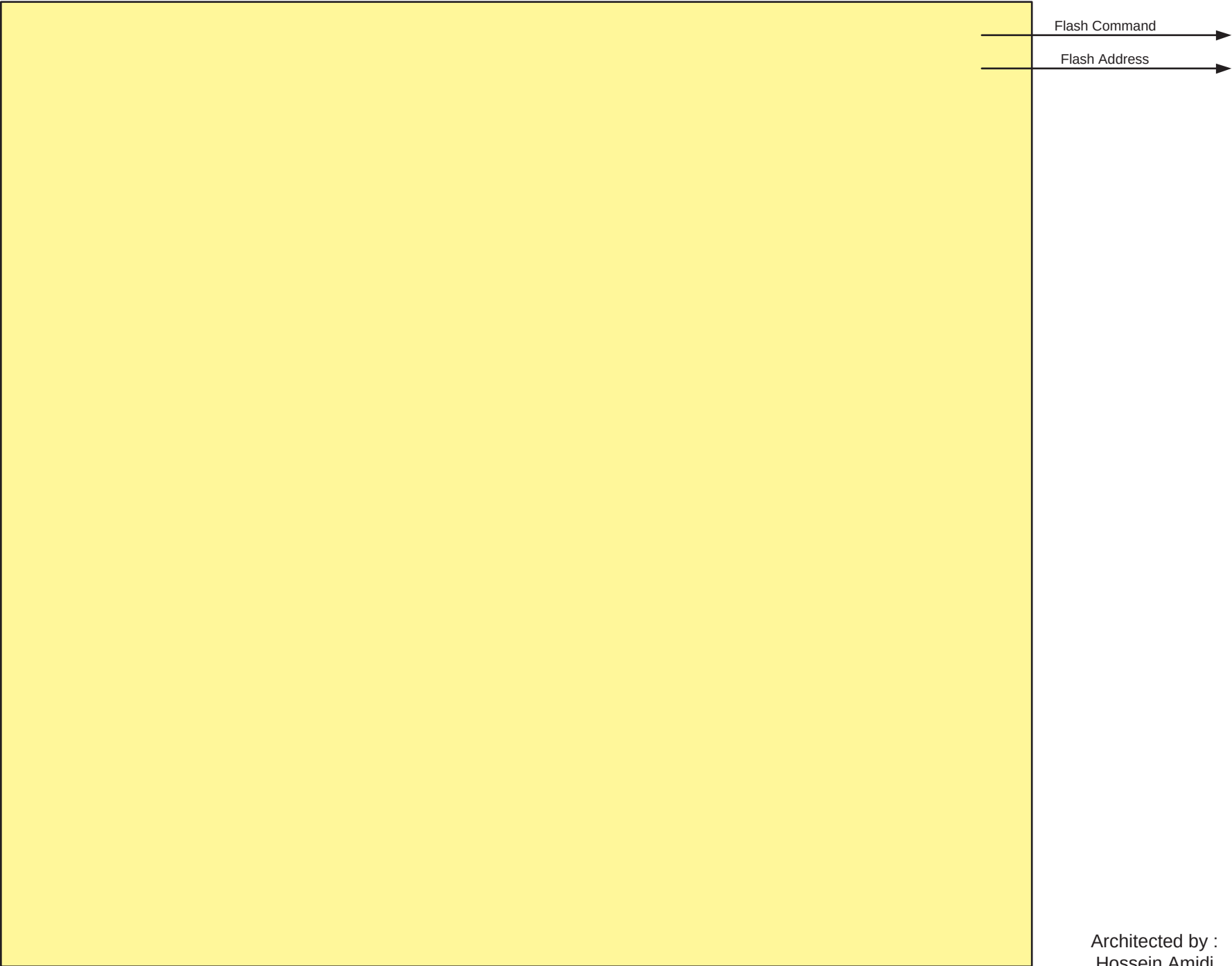
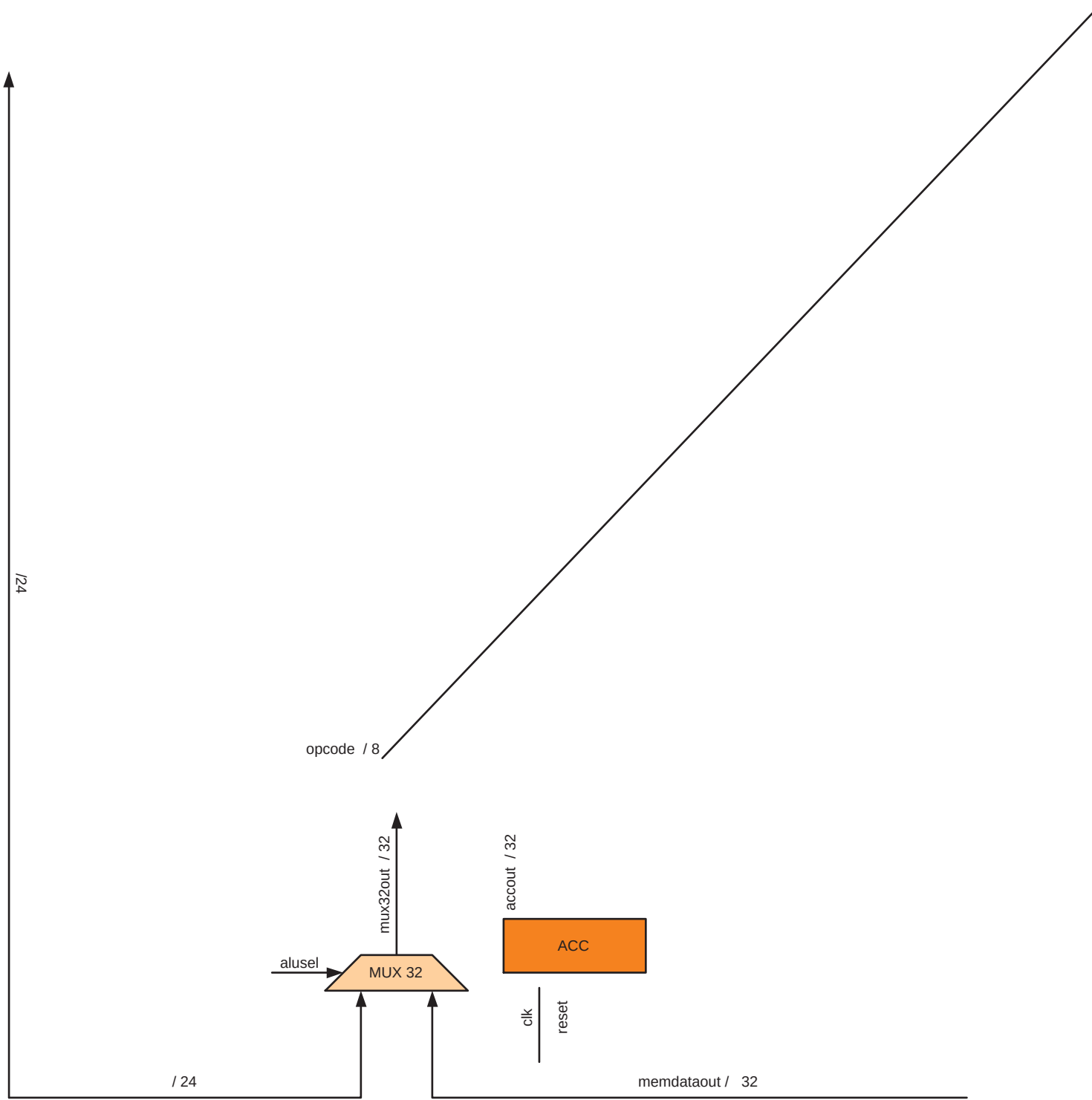


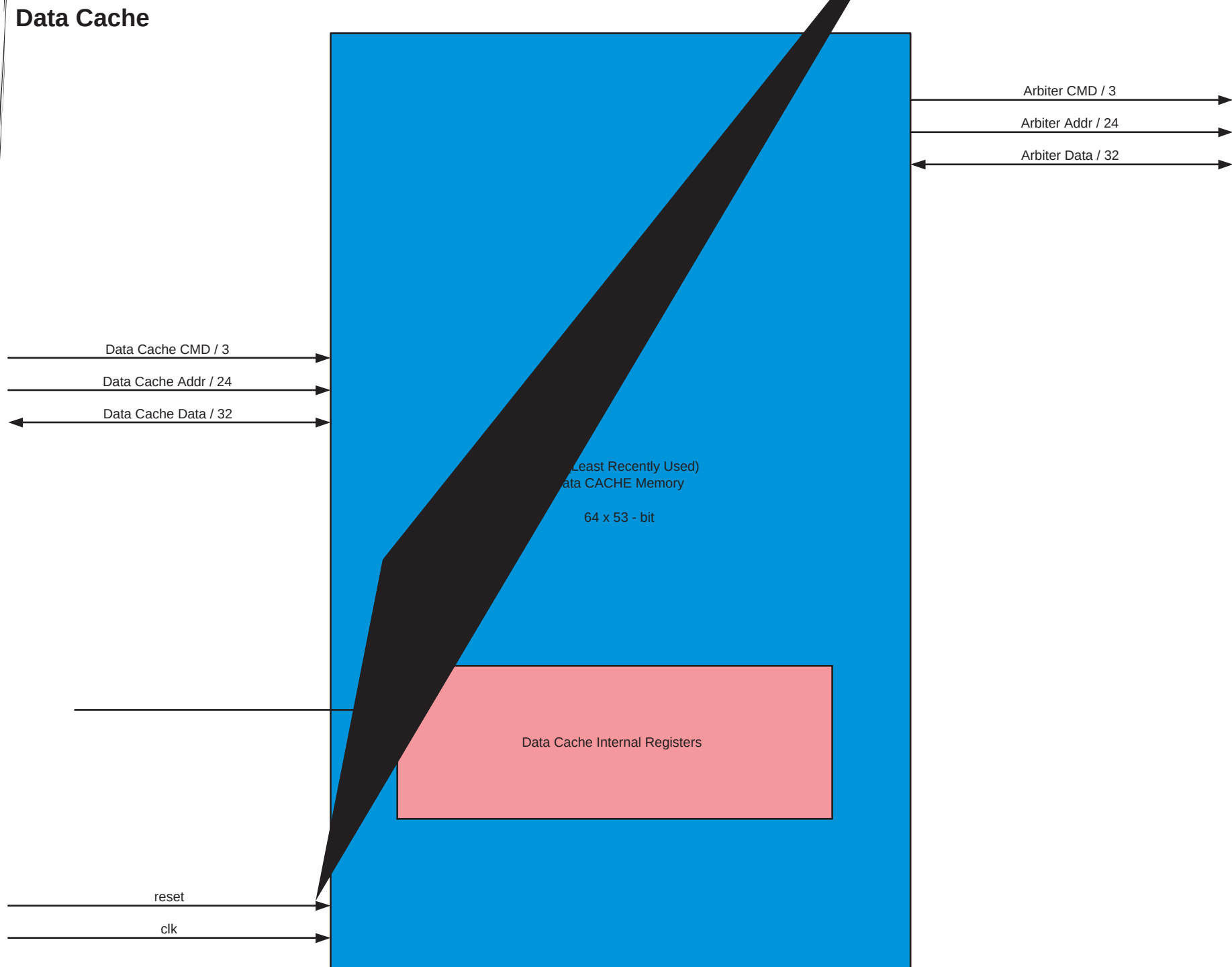
System On a Chip



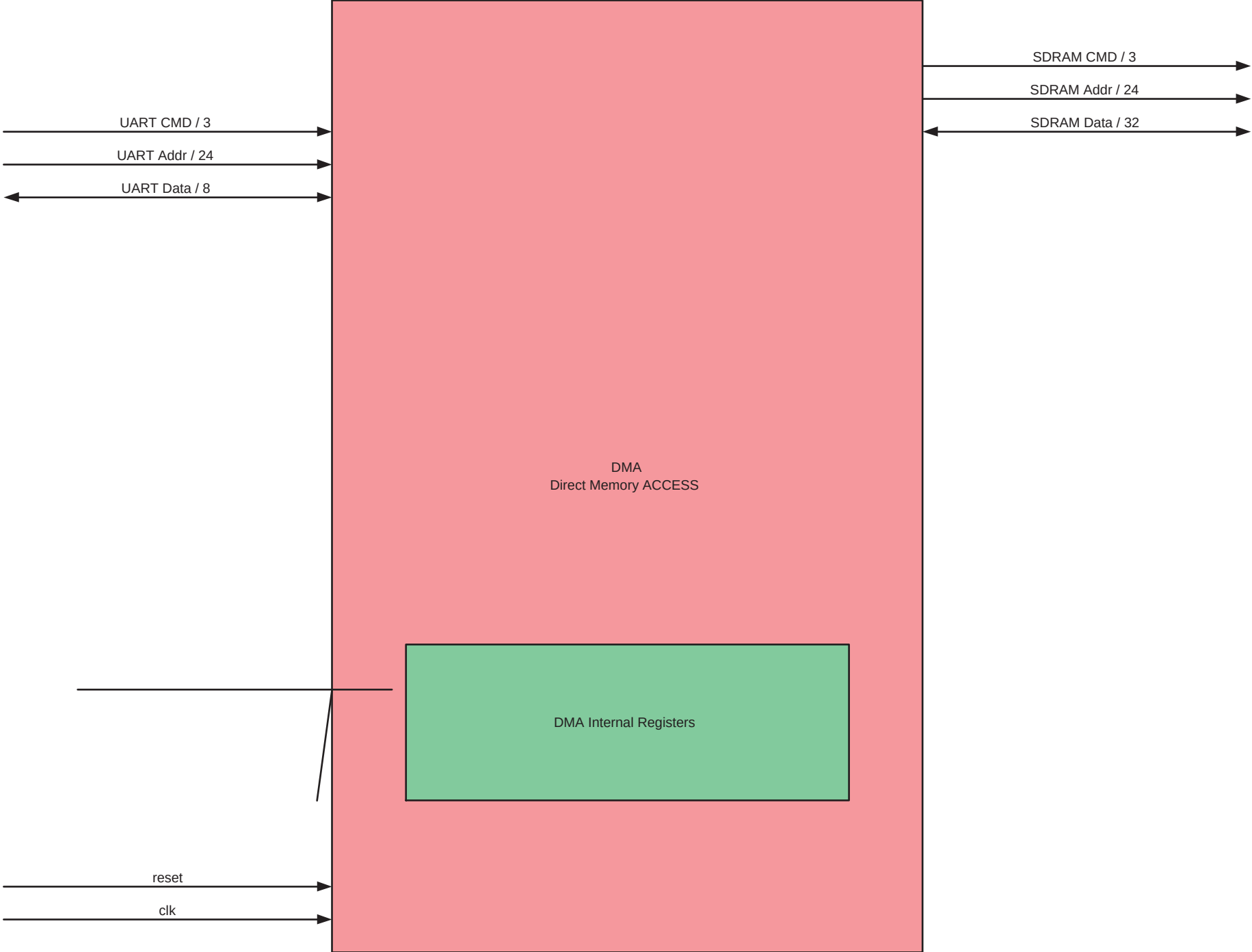


BUS Arbiter

Data Cache



Direct Memory Access



SDRAM Controller

Memory Map

